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**IN THE SPECIFICATION**

Please replace the paragraph beginning on page 11, line 29 and ending on page 12, line 12 with the following amended paragraph:

IDC-A1,AMD

Turning now to FIG. 4B, described is a function control flow diagram 408. This is a detailed description of the step 408 in FIG. 4A. The flow is entered at step 420 when there is a need to connect the profiling tool to a running application 302 at step 424. Node A 422 is the point at which a stopped profiling execution can be restarted. The application's source code structure is displayed at step 426. It is noted that this is not the source code but the function level view. The source code is not itself needed, nor is any recompiling or relinking of the target application necessary. Node B 428 is the point at which a new setup for profiling can be applied, without stopping the target application. At step 430 the CPU profiling and function call count is applied the desired functions, regardless of whether they are application functions or system library functions. Once CPU profiling is turned on, an instrumentation point is selected, step 432, this ~~[[it]]~~ applies to every function that is executed, until either it is turned off, or the application completes its execution. Node C 434 is the point where DPCL probes can be turned on or off. Step 436 shows the creation of a DPCL probe that can retrieve intermediate results. The profiling can now begin at step 438.

Please replace the paragraph beginning on page <sup>18</sup>~~19~~, line <sup>13</sup>~~5~~ with the following amended paragraph:

IFW SENT  
RW  
5/24/06

IDC-A2,AMD

Turning now to FIG. 7 containing table ~~[[700]]~~ 702, the flat profile sample is the second part of the cwhet.gprof file.